

IN THE UNITED STATES PATENT AND TRADEMARKS OFFICE

**Patent Application**

Inventor(s) Yih-Feng Chyan  
John Michael Hergenrother  
Donald Paul Monroe

Case 15-6-9

Serial No. 09/648164 Group Art Unit

Filing Date August 25, 2000

Examiner

Title Architecture for Circuit Connection of a Vertical Transistor

**ASSISTANT COMMISSIONER FOR PATENTS**  
**WASHINGTON, D. C. 20231**

SIR:

**PETITION FOR EXTENSION OF TIME**

Applicant(s) petition(s) the Commissioner of Patents and Trademarks to extend the time for response to the Office action dated 10/30/2000 for 1 (one) month from 12/30/01 to 1/30/01.

Please charge **Lucent Technologies Inc.** Deposit Account No. **12-2325** in the amount of \$110.00 to cover the cost of the extension. Any deficiency or overpayment of the required fee should be charged or credited to **Deposit Account No. 12-2325** as required to correct the error. Duplicate copies of this petition are enclosed.

01/31/2001 BHABTEW 00000080 122325 09648164

01 FC:115 110.00 CH

Date: 1/22/01



Ferdinand M. Romano  
Reg. No. 32752  
Attorney for Applicant

Lucent Technologies Inc.  
Docket Administrator  
101 Crawfords Corner Road Room 3J-219  
Holmdel, New Jersey 07974-0636

